

# PURDUE UNIVERSITY NORTH CENTRAL

*Electrical & Computer Engineering Technology Department*

ECET 15700 (Prof. Smith)

SYLLABUS (Weeks 1 – 6)

Spring, 2012

<b>W K</b>	<b>DATE</b> (Lab Topic)	<b>CHAPTER SECTIONS TO BE COVERED</b> (Please read this before class.)	<b>HOMEWORK</b> (Due on this date)
1	Jan 9	Introduction; Begin Ch 26 - Semiconductors	---
1	Jan 11	Finish Ch 26 - Semiconductors	---
1	Lab	(No lab the first week of semester.)	---
2	Jan 16	<b>Martin Luther King, Jr. Holiday - - NO SCHOOL</b>	---
2	Jan 18	Ch 27 - Diode Theory & Applications	---
2	Lab	(No lab - - Martin Luther King, Jr. Holiday)	---
3	Jan 23	Go over Homework Problems for Ch 27	Ch 27: 1, 2, 29, 30, 33 & 41.
3	Jan 25	Ch 28 – Transistor Theory (thru BJTs) – Sections 1 - 3	---
3	Lab	Math Review - - Sine Waves	---
4	Jan 30	Ch 28 (cont) – Sections 4 & 5	Ch 28: 1, 2, 3, 4 & 9
4	Feb 1	Ch 28 (cont) – Sections 6 – 8 (Practice Test # 1 Available on Class Website)	Ch 28: 12, 15, 16 & 17. (See website for printed BJT characteristic curve sheets.)
4	Lab	Lab 2 – F-W Rectifier Ckt (Analyze w/ LT or PSPICE)	---
5	Feb 6	Go Over Practice Test #1	Ch 28: 21, 31, 43 & 45.
5	Feb 8	Test #1 – Cover Ch 26, 27 & 28 (thru BJTs)	---
5	Lab	Lab 2 (cont.) – Build Full-Wave Rectifier Circuit	---
6	Feb 13	Go Over Test #1	---
6	Feb 15	Ch 30 - OpAmps - - Section 1	---
6	Lab	Lab 3 – Transistor Amp Ckt (Analyze w/ LT or PSPICE)	---

W K	DATE (Lab Topic)	CHAPTER SECTIONS TO BE COVERED (Please read this before class.)	HOMEWORK (Due on this date)
10		Spring Break - - NO SCHOOL	- - -
10		Spring Break - - NO SCHOOL	- - -
10		Spring Break - - NO SCHOOL	- - -
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**FINAL EXAM WEEK**

**NOTE:** Final portfolio is due the evening of the final exam.  
 It must contain the following: all homework, all lab reports and all tests ***in that order.***  
 Portfolio ***must*** be in a binder.  
 There ***must*** be a labelled, tabbed divider page beginning each section.  
 There ***must*** be a title page ***inside*** the binder, with your name, class title, session & year.